

PATENT APPLICATION

042390.P5271

Amendment

Amendment to Specification

Please add the following to the beginning of the specification:

a1 This is a continuation of application 08/993,104 filed on December 18, 1997.

Amendment to Claims

Please amend the claims as shown below.

- Sub
B1
- Q2
Cmt
1. (Currently Amended) A circuit for modulating voltage signals comprising:
a first circuit configuration to substantially simultaneously [and asynchronously] drive respective positive and negative voltage signals onto respective first and second voltage signal storage elements, wherein the first circuit is adapted to apply the respective positive and negative voltage signals onto the first and second voltage storage elements independently of application of voltage signals to other storage elements;
and a second circuit configuration to alternatively sample the respective voltage signals of the respective voltage signal storage elements at a substantially predetermined rate.
 2. (Original) The circuit of claim 1, and further comprising a liquid crystal cell coupled to said second circuit configuration.
 3. (Original) The circuit of claim 2, wherein the substantially predetermined rate is related, at least in part, to the particular liquid crystal material of the liquid crystal cell.
 4. (Original) The circuit of claim 2, wherein said first circuit configuration includes circuitry to address said liquid crystal cell.

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5. (Currently Amended) The circuit of claim 4, wherein said circuit for modulating voltage signals is coupled in a liquid crystal display (LCD) system; said LCD system being adapted to substantially simultaneously and [asynchronously] independently drive additional voltage signals onto the respective first and second voltage signal storage elements so that the stored voltage signals of the respective first and second voltage signal storage elements are refreshed.

6. (Currently Amended) The circuit of claim 2, wherein said second circuit comprises a plurality of transistors coupled to electrically isolate said first and second voltage signal storage elements from said liquid crystal cell while alternatively sampling the respective voltage signals of the respective first and second voltage signal storage elements.

7. (Currently Amended) The circuit of claim 1, wherein the first and second voltage signal storage elements comprise capacitors.

8. (Original) The circuit of claim 1, wherein said circuit for modulating voltage signals is embodied on an integrated circuit chip.

9. (Currently Amended) A liquid crystal display (LCD) system comprising:

a voltage signal modulation circuit to locally modulate the voltage signal applied across a liquid crystal cell in said LCD system;

said voltage signal modulation circuit including a first circuit configuration to substantially simultaneously and [asynchronously] independently drive respective positive and negative voltage signals onto respective voltage signal storage elements and a second circuit configuration to alternatively sample the respective voltage signals of the respective voltage signal storage elements at a substantially predetermined rate.

10. (Original) The LCD system of claim 9, and further comprising at least one liquid crystal cell coupled to said voltage signal modulation circuit.

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11. (Original) The LCD system of claim 10, wherein the substantially predetermined rate is related, at least in part, to the particular liquid crystal material of the liquid crystal cell.

12. (Original) The LCD system of claim 10, wherein said system includes circuitry to address said at least one liquid crystal cell.

13. (Currently Amended) The LCD system of claim 10, wherein said LCD system is adapted to substantially simultaneously and [asynchronously] independently drive additional voltage signals onto the respective voltage signal storage elements so as to refresh the stored voltage signals.

14. (Currently Amended) A method of modulating a voltage signal locally comprising:

applying respective positive and negative voltage signals to respective voltage signal storage elements substantially simultaneously [and asynchronously]; and

sampling the voltage signals of the respective voltage storage elements alternatively at a substantially predetermined rate.

16. (Original) The method of claim 15, wherein the substantially predetermined rate is related, at least in part, to the particular liquid crystal cell material of the liquid crystal cell.

17. (Original) The method of claim 14, wherein the voltage signal storage elements comprise capacitors.

18. (Currently Amended) A voltage signal modulation circuit comprising:

a first circuit to substantially simultaneously [and asynchronously] drive respective voltage signals onto respective voltage signal storage elements; and

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a second circuit to sample the voltage signals of the respective voltage signal storage elements so as to locally produce a modulated voltage signal.

19. (Currently Amended) The voltage signal modulation circuit of claim 18, wherein the voltage signals comprise respective positive and negative voltage signals and the respective voltage signal storage elements comprise two respective voltage signal storage elements;

said first circuit being adapted to substantially simultaneously [and asynchronously] drive the respective positive and negative voltage signals onto the two respective voltage signal storage elements.

20. (Original) The voltage signal modulation circuit of claim 18, wherein said second circuit is adapted to sample the voltage signals of the respective voltage signal storage elements at a substantially predetermined rate.

21. (Original) The circuit of claim 18, wherein said second circuit is further adapted to sample the voltage signals of the respective voltage signal storage elements so as to substantially maintain a substantially DC bias.

22. (Currently Amended) A method of modulating a voltage signal locally comprising:

applying respective voltage signals to respective voltage signal storage elements substantially simultaneously [and asynchronously]; and

sampling the voltage signals of the respective voltage signal storage elements at a substantially predetermined rate so as to locally produce the modulated voltage signal.

23. (Original) The method of claim 22, wherein the voltage signals of the respective voltage signal storage elements are sampled so as to substantially maintain a substantially DC bias.

24. (Currently Amended) A display system comprising:

a voltage signal modulation circuit to locally modulate the voltage signal applied across a light modulating element in said display system;

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said voltage signal modulation circuit including a first circuit configuration to substantially simultaneously [and asynchronously] drive respective voltage signals onto respective voltage signal storage elements and a second circuit configuration to sample the voltage signals of the respective voltage signal storage elements at a substantially predetermined rate so as to locally produce a modulated voltage signal.

25. (Currently Amended) The system of claim 24, wherein said system is adapted to drive substantially simultaneously [and asynchronously] additional voltage signals onto the respective voltage signal storage elements so as to refresh the stored voltage signals.
